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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application Number	10 / 656,823
	Filing Date	September 4, 2003
	First Named Inventor	Vladislav Vashchenko
	Group Art Unit	
	Examiner Name	
Total Number of Pages in This Submission	Attorney Docket Number	P05199 CIP

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Jurgen Vollrath
Signature	
Date	9/30/2003

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J. Vollrath  
Jurgen Vollrath

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of	)	
	)	Art Unit:
Inventor: Vladislav Vashchenko et al.	)	
	)	
Serial No.: 10/656,823	)	
	)	Examiner:
Filed: September 4, 2003	)	
	)	
Title: ESD PROTECTION SNAPBACK	)	
STRUCTURE FOR OVERVOLTAGE SELF-	)	
PROTECTING I/O CELLS	)	

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**SUPPLEMENTAL APPLICATION DATA SHEET**

This is supplementary to the priority information provided at time of filing the above application as continuation-in-part application of 10/097,283.

1. Applicant information – unchanged as per declaration filed with application
2. Correspondence information – unchanged as per declaration filed with application
3. Application information:
  - a. Title: ESD PROTECTION SNAPBACK STRUCTURE FOR OVERVOLTAGE SELF-PROTECTING I/O CELLS
  - b. Classification: 438/202, 438/201
  - c. Drawing sheets: 3
  - d. Suggested drawing for publication: Figure 4
  - e. Docket number: P05199 CIP
  - f. Type: Utility
  - g. Application does not disclose anything under a secrecy order
4. Representative: Jorgen Vollrath (Reg. # 49,098)
5. Domestic Priority information: Continuation-in-Part application of and claims priority from U.S. Application Serial Number 10/097,283 which was filed March 12, 2002 and which has been abandoned in favor of the present CIP.
6. Foreign Priority information: None
7. Assignee information: National Semiconductor Corporation, 2900 Semiconductor Drive, Mail Stop D3-579, Santa Clara, 95051

Respectfully Submitted,

Dated: 9/30/, 2003

  
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